

NARAYANA ENGINEERING COLLEGE::NELLORE

REPORT ON GUEST LECTURE

1	Name of the Activity/Event	“Digital Electronics”		
2	Date of Activity/Event	4/8/2018 & 5/8/2018		
3	Organized by/Name of the committee	EEE Department		
4	Place of Activity/event	Edison Auditorium		
5	Resource person/guest/organization	K.Gangadhar Reddy, GATE academy , Hyderabad		
6	Type of activity/Event	Guest lecture		
7	Activity/Event objectives	Importance of Digital electronics		
8	Participation	Students	Faculty	Total Participation
		100	03	103
9	General remarks	1.Need more Explanation 2.Fast Delivery		
10	Suggested Improvements	Need full day session		
11	Enclosures	1.Report 2.Photos 3.Attendance		

A Guest lecture on “Digital Electronics was organized by Electrical and Electronics Engineering department, on 4th & 5th August 2018 at the Edison Auditorium, Narayana Engineering College, Nellore. In this session they discussed about Nowadays, a large proportion of the activity performed by an electronic device occurs inside integrated circuits. Consequently, the difference between analog and digital circuits is rooted in the difference between analog and digital *integrated* circuits.

Analog and digital ICs contain the same basic components: primarily transistors, but also diodes and passive elements. However, in analog ICs, transistors are intended to amplify or produce continuously varying signals. When we bias a transistor, we create circuit conditions that allow it to properly respond to small changes in voltage. For example, an input stage of an amplifier IC might employ the MOSFET differential-pair configuration shown below; note that the current source (I_{BIAS}) is biasing the Q1 and Q2 transistors. the word “digital” refers in a general way to systems involving a limited set of discrete values, in the context of modern electronics, “digital” implies binary. In binary calculations, the only available digits are one and zero, and this mathematical construct is translated into the electronic domain through the use of digital circuitry in which voltages are always “high” or “low.”



HOD

From
K.Gangadhar Reddy
GATE Academy
Hyderabad

To
The EEE-HOD,
Electrical & Electronics Engineering,
Narayana Engineering College
Nellore.

Respected sir,

Sub: Tolerant your letter dated 31.07.2018-Reg.

Thank you for inviting me to conduct Guest lecturer on “Digital electronics”.
We are very glad to accept your invitation and this letter is to confirm you for your requisition dated on 04/08/2018 & 05/8/2018. I hope the lecture helpful to your students on 08/10/18.

Thanking you.

Place: Nellore

Yours Sincerely,
Mr.K.GandharReddy